CRYSTAL CLOCK OSCILLATORS

Data Sheet 0210B

Rev. L

LVCMOS (1.8V)
SC-C1440 Series

Description

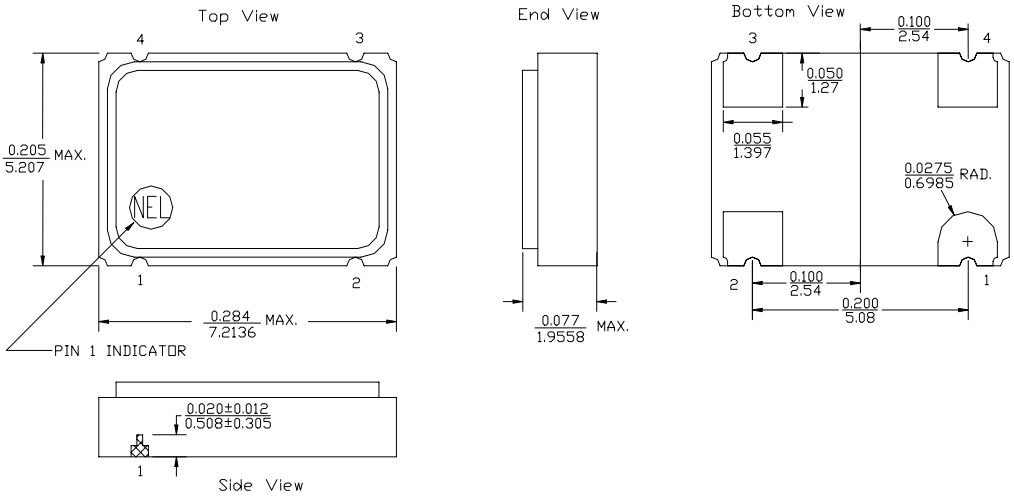
The **SC-C1440 Series** of quartz crystal oscillators provide enable/disable 3-state LVCMOS compatible signals for bus connected systems. Supplying Pin 1 of the SC-C1440 units with a logic "1" or open enables its Pin 3 output. In the disable mode, Pin 3 presents a high impedance to the load.

Features

- Wide frequency range–70.0MHz to 165.0MHz
- User specified tolerance available
- Space-saving alternative to discrete component oscillators
- 1.8 Volt operation
- High shock resistance, to 1000g
- High Q Crystal actively tuned oscillator circuit
- COTS/Dual use
- High Reliability - NEL HALT/HASS qualified for crystal oscillator start-up conditions
- Low Jitter - Wavecrest jitter characterization available
- Power supply decoupling internal
- No internal PLL avoids cascading PLL problems
- High frequencies due to proprietary design
- Metal lid electrically connected to ground to reduce EMI
- Gold plated pads
- RoHS Compliant, Lead Free Construction

Electrical Connection

Pin	Connection
1	Enable/Disable
2	Ground
3	Output
4	V_{DD}



NEL FREQUENCY CONTROLS, INC.

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SC-C1440 Series Continued
LVCMOS

Rev. L

Operating Conditions and Output Characteristics

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max
Frequency	-----	-----	70.0MHz	-----	165.0MHz
Duty Cycle	-----	@ V_{DD}/2	45/55%	-----	55/45%
Logic 0	V_{OL}	@ 600µA	-----	-----	0.2V
Logic 1	V_{OH}	@ 600µA	V_{DD}-0.2V	-----	-----
Rise & Fall Time	tr,tf	10-90%V_O	-----	-----	3.5 ns
Jitter, Integrated	J	Integrated from phase noise, 12kHz to 20MHz, RMS	-----	0.1 ps	-----
Jitter, Wavecrest Characterized (2)	-----	Random Period	-----	2.3ps	-----
		Accum, pk-to-pk	-----	26ps	-----
Phase Noise (4)	£(Δf)	@ 10Hz	-----	-70 dBc/Hz	-----
		@100Hz	-----	-105 dBc/Hz	-----
		@1kHz	-----	-130 dBc/Hz	-----
		@10kHz	-----	-145 dBc/Hz	-----
		@100kHz	-----	-150 dBc/Hz	-----
		@ >1Mhz	-----	-150 dBc/Hz	-----
T_{pz}	-----	-----	-----	-----	100 ns
Enable Voltage	-----	-----	1.3V	-----	-----
Disable Voltage	-----	-----	-----	-----	0.5V
Frequency Stability (1)	dF/F	Overall conditions including: voltage, calibration, temp., 10 yr aging, shock, vibration	-100ppm	-----	+100ppm

General Characteristics

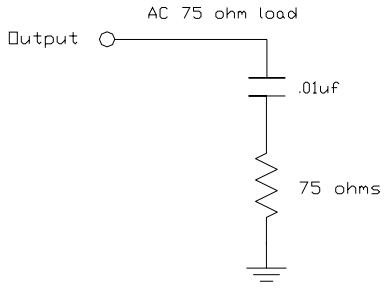
Parameter	Symbol	Conditions	Min	Typical	Max
Supply Voltage (3)	V_{DD}	-----	1.71V	1.8V	1.89V
Supply Current	I_{DD}	No Load	0.0 mA	25 mA	40 mA
Output current	I_O	Low level Output Current	0.0 mA	-----	±25.0 mA
Operating temperature	T_A	------	0°C	-----	70°C
Storage temperature	T_S	-----	-55°C	-----	125°C
Power Dissipation	P_D	-----	-----	-----	76 mW
Load	-----	-----	-----	-----	15pf
Start-up Time	t_s	-----	-----	-----	10 ms

Environmental and Mechanical Characteristics

Mechanical Shock	Per MIL-STD-202, Method 213, Condition E
Thermal Shock	Per MIL-STD-883, Method 1011, Condition A
Vibration	0.060" double amplitude 10 Hz to 55 Hz, 35g's 55Hz to 2000 Hz
Hermetic Seal	Leak rate less than 1 x 10^{-8} atm.cc/sec of helium

Footnotes:
1) Standard frequency stability (±20,±25,±50ppm & others available)
2) Jitter performance is frequency dependent. Please contact factory for full Wavecrest characterization.
3) Internal high frequency power source decoupling.
4) If phase noise data at a particular frequency is needed, contact factory.

Test Load:



Creating a Part Number

SC - C144X - FREQ

Package Code
SC 4 pad 5x7mm SMD

Input Voltage

Code	Specification
A	3.3V
B	2.5V
C	1.8V
	5.0V

Tolerance/Performance

0	±100ppm 0-70°C
1	±50ppm 0-70°C
7	±25ppm 0-70°C
9	Customer Specific
A	±20ppm 0-70°C
B	±50ppm -40 to +85°C
C	±100ppm -40 to +85°C

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Max Reflow Profile